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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

TIRDAD SOWLATI ET AL.

US000099

Serial No. 09/545,785

Group Art

Filed: APRIL 7, 2000

Examiner



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7-24-00
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INTERDIGITATED MULTILAYER CAPACITOR STRUCTURE FOR DEEP SUB-MICRON CMOS

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

LETTER TO OFFICIAL DRAFTSMAN

Sir:

Enclosed are FOUR (4) sheets of formal drawing
for filing in the above-identified application.

Respectfully submitted,

By Michael E. Marion, Reg. 32,266
Attorney
(914) 333-9641

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited this date with the United States Postal Service as
first-class mail in an envelope addressed to:

COMMISSIONER OF PATENTS AND TRADEMARKS
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On June 19, 2000
(Mailing Date)
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